

MODIS PI PROCESSING MEETING

Agenda June 9, 1999

- 9:30 a.m. Review of action items from the last meeting – 15 minutes
- 9:45 a.m. MODAPS test plan update (Ed Masuoka) – 20 minutes
- 10:05 a.m. SSTG plans (Mike Linda) – 20 minutes
- 10:25 a.m. V1 MODAPS Development status/loaders (Gang Ye) – 20 minutes
- 10:45 a.m. Break – 5 minutes
- 10:50 a.m. X day test status/results (SDDT Leads/Ed Masuoka) – 30 minutes
- 11:20 a.m. MOSS-2 test update (Tan Aslam/DAACs) – 15 minutes
- 11:35 a.m. Y test proposal (Robert Wolfe/DAACs) – 15 minutes
- 12-2:00 p.m. Breakout working group on L1 product suites
(Francesco Bordi, Chair)
- 2:00 p.m. Operational procedures (Mike Teague/Sol Broder) – 15 minutes
- 2:15 p.m. MODAPS hardware plans/schedule (MODULAR, MO1,MTVS1)
(Ed Masuoka) – 15 minutes
- 2:30 p.m. SIPS “deliver” interface status/ingest of GDAAC products into
MODAPS /Error Handling (Tan Aslam/GDAAC) – 10 minutes
- 2:40 p.m. Break
- 2:50 p.m. SIPS “insert” interface status (ingest of MODAPS products in
DAACs) (Tan Aslam/Mike Jones/Shirley Read/DAACs) – 30
minutes
- 3:20 p.m. Update from the DAACs on processing, archival, and distribution
test schedule (DAACs) -10 minutes each
- 3:50 p.m. Ancillary data status (S. Read) – 20 minutes
- 4:10 p.m. Versioning discussion (multiple version ESDT) (Ed Masuoka/
Robert Wolfe/DAACs) – 10 minutes